Remarks

Reconsideration of this Application is respectfully requested.

Upon entry of the foregoing amendment, claims 1, 3-17, 19-63, and 65-66 are pending in the application, with claims 1, 17, 61-63, and 65-66 being the independent claims. Claims 1, 3, 4, 6, 17, 19, 27, 28, 49, 57, 61, and 62 are amended herein. Claims 2, 18, and 64 are sought to be cancelled without prejudice to or disclaimer of the subject matter provided therein. These changes are believed to introduce no new matter, and their entry is respectfully requested.

Based on the above amendment and the following remarks, Applicants respectfully request that the Examiner reconsider all outstanding objections and rejections and that they be withdrawn.

Objection to the Specification

The abstract is objected to for being in excess of 150 words. The abstract has been amended herein to overcome this objection.

Rejections under 35 U.S.C. § 102

Claims 1-2, 5-6, 11-13, 16, 49-50, 59, 60, and 63 are rejected under 35 U.S.C. 102(b) as being anticipated by Azadet et al. (EP 1006 697 A2), hereinafter referred to as "Azadet".

Independent claim 1, as amended, recites, among other features, "individually adjusting one or more parameters for each of said N ADC paths, <u>including individually adjusting said N sampling signals to reduce phase errors</u> between said received data

signal and each of said N sampling signals in said N ADC paths". In rejecting claim 1, the Examiner claims that the receiver of Azadet is capable of having each of the ADC paths adjusted with one or more parameters in the error estimation circuits 340-1 through 340-N, gain control circuits 350-1 through 350-N, and offset cancellation circuits 360-1 through 360-N. However, nowhere does Azadet teach or suggest individually adjusting the N sampling signals to reduce phase errors, as recited in claim 1, or in the least adjusting the sampling signals at all in the ADC paths. Accordingly, Azadet does not anticipate claim 1, as amended. Reconsideration and withdrawal of the rejection of claim 1 is respectfully requested.

Claims 5-6, 11-13, 16, 49, 50, 59, and 60 depend directly or indirectly from claim 1. For at least the reasons provided above with respect to claim 1, claims 2, 5-6, 11-13, 16, 49, 50, 59, and 60 are not anticipated by Azadet. Reconsideration and withdrawal of the rejection of claims 5-6, 11-13, 16, 49, 50, 59, and 60 is respectfully requested.

Claim 2 is sought to be cancelled herein without prejudice to or disclaimer of the subject matter provided therein. The rejection of claim 2 is thus rendered moot

Independent claim 63 recites, among other features, "individually adjusting one or more parameters for each of said N ADC paths, including individually adjusting at least a sampling phase in each of said N ADC paths to compensate for phase errors between each said N sampling signals and said received data signal". As provided above with respect to claim 1, Azadet does not teach or suggest adjusting the sampling signals in the ADC paths. Further, Azadet does not teach or suggest individually adjusting at least a sampling phase in each of said N ADC paths as recited in claim 63. For at least

this reason, Azadet does not anticipate claim 63. Reconsideration and withdrawal of the rejection of claim 63 is respectfully requested.

Rejections under 35 U.S.C. § 103

Claims 17-19, 31-32, 37-39, 42, 57-58, 61-62, and 64-66 are rejected under 35 U.S.C. 103(a) as being unpatentable over Azadet et al. (EP 1006 697 A2), hereinafter referred to as "Azadet".

Independent claim 17, as amended, recites among other features, "means for individually adjusting one or more parameters for each of said N ADC paths, including means for adjusting each of said N sampling signals to reduce sampling phase errors in said N ADC paths". In rejecting claim 17, the Examiner claims that the receiver of Azadet is capable of having each of the ADC paths adjusted with one or more parameters in the error estimation circuits 340-1 through 340-N, gain control circuits 350-1 through 350-N, and offset cancellation circuits 360-1 through 360-N. However, nowhere does Azadet teach or suggest having a means for adjusting each of the N sampling signals to reduce sampling phase errors, as recited in claim 17, or in the least means for adjusting the sampling signals at all in the ADC paths. Accordingly, claim 17 is patentable over Azadet. Reconsideration and withdrawal of the rejection of claim 17 is respectfully requested.

Claims 19, 31, 32, 37, 39, 42, 57, and 58 depend directly or indirectly from claim 17. Claims 19, 31, 32, 37, 39, 42, 57, and 58 are therefore patentable over Azadet. Reconsideration and withdrawal of the rejection of claims 19, 31, 32, 37, 39, 42, 57, and 58 is respectfully requested.

Claim 18 is sought to be cancelled herein without prejudice to or disclaimer of the subject matter provided therein. The rejection of claim 18 is thus rendered moot.

Independent claims 65 and 66 both recite, among other features, "individually adjusting one or more parameters for each of said N ADC paths using said control signals, including individually adjusting at least a sampling phase in each of said N ADC paths to compensate for phase errors between each said N sampling signals and said received data signal". As provided above with respect to claim 1, Azadet does not teach or suggest adjusting the sampling signals in the ADC paths. Further, Azadet does not teach or suggest individually adjusting at least a sampling phase in each of said N ADC paths as recited in both claims 65 and 66. For at least this reason, claims 65 and 66 are patentable over Azadet. Reconsideration and withdrawal of the rejection of claim 65 and 66 is respectfully requested.

Independent claims 61 and 62, as amended, recite similar features of individually adjusting at least a sampling phase in each of the N ADC paths, as recited in claims 65 and 66. For at least reasons provided above with respect to claim 65 and 66, claims 61 and 62 are patentable over Azadet. Reconsideration and withdrawal of the rejection of claims 61 and 62 is respectfully requested.

Claim 64 is sought to be cancelled herein without prejudice to or disclaimer of the subject matter provided therein. The rejection of claim 64 is thus rendered moot.

Claims Objections

Claim 6 is objected to for missing a period at the end. Claim 6 has been amended herein to overcome this objection.

Claims 3-4, 7-10, 14-15, 20-30, 33-36, 40-41, 43-48, and 51-56 stand objected to as being dependent upon a rejected base claim, but allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 3, 4, 7-10, 14-15, and 43-48 depend directly or indirectly from claim 1. For at least reasons provided above with respect to claim 1, claims 3, 4, 7-10, 14-15, and 43-48 are patentable. Reconsideration and withdrawal of the objection to claims 3, 4, 7-10, 14-15, and 43-48 is respectfully requested.

Claims 20-30, 33-36, 40-41, and 51-56 depend directly or indirectly from claim 17. For at least reasons provided above with respect to claim 17, claims 20-30, 33-36, 40-41, and 51-56 are patentable. Reconsideration and withdrawal of the objection to claims 20-30, 33-36, 40-41, and 51-56 is respectfully requested.

Conclusion

All of the stated grounds of objection and rejection have been properly traversed, accommodated, or rendered moot. Applicants therefore respectfully request that the Examiner reconsider all presently outstanding objections and rejections and that they be withdrawn. Applicants believe that a full and complete reply has been made to the outstanding Office Action and, as such, the present application is in condition for allowance. If the Examiner believes, for any reason, that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at the number provided.

Prompt and favorable consideration of this Amendment and Reply is respectfully requested.

Respectfully submitted,

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